

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
Shunpei YAMAZAKI et al.) ATTN: New Applications
Based On: JP 2002-276379)
Filed: September 20, 2002)
For: MANUFACTURING METHOD)
OF SEMICONDUCTOR DEVICE)

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

U.S. Patent No. 6,475,912 is in the family of Japanese Patent Laid-Open No. 2000-58650.

U.S. Patent Publication 2001/0015618 is in the family of Japanese Patent Laid-Open No. 2001-236025.

U.S. Patent Publication 2001/0030322 is in the family of Japanese Patent Laid-Open No. 2001-313397.

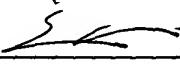
U.S. Patent No. 6,413,845 is in the family of Japanese Patent Laid-Open No. 2000-357671.

The Examiner's attention is also directed to related U.S. Patent Application Serial No. 10/245,701.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be

required now or hereafter, or credit any overpayment to Deposit Account No. 50-2280.
A duplicate copy of this sheet is attached.

Respectfully submitted,



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| INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i> | | | | | |
| Sheet | 2 | of | 2 | Examiner Name | |
| | | | | Attorney Docket Number | 0756-7196 |

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Approved for use through 10/31/2002. OMB 0651-0031

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| Sheet | 1 | of | 2 | Filing Date | September 10, 2003 |
| | | | | First Named Inventor | Shunpei YAMAZAKI et al. |
| | | | | Group Art Unit | |
| | | | | Examiner Name | |
| | | | | Attorney Docket Number | 0756-7196 |

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